

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		display and (TFT or thin adj film adj transistor) and and DC and voltage and converter and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:27
S1	809	345/204.ccls.	USPAT; EPO; JPO	OR	OFF	2004/01/27 08:16
S2	1	"5,532,712".pn.	USPAT; EPO; JPO	OR	OFF	2003/02/26 11:47
S3	277	drive adj circuit and transmissive and liquid adj crystal adj display	USPAT; EPO; JPO	OR	OFF	2003/02/26 11:59
S4	1	drive adj circuit and transmissive adj scattered and liquid adj crystal adj display	USPAT; EPO; JPO	OR	OFF	2003/02/26 11:59
S5	15	drive adj circuit and transmissive and liquid adj crystal adj display and ambient adj temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 12:00
S6	101079	liquid adj crystal adj display	USPAT; EPO; JPO	OR	OFF	2004/06/03 16:02
S7	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	OR	OFF	2003/02/26 12:48
S8	3	liquid adj crystal adj display and charge adj characteristic and compensate and circuit and ambient	USPAT; EPO; JPO	OR	OFF	2003/02/26 12:49
S9	3	liquid adj crystal adj display and charge adj characteristic and compensate and circuit and ambient adj temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 12:50
S10	15	liquid adj crystal adj display and charge adj characteristic	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:03
S11	2	liquid adj crystal adj display and charge adj characteristic and gate and control and temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:05
S12	2	345/87.ccls. and temperature adj ambient	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:08
S13	3	liquid adj crystal adj display and charge adj characteristic and gate and control	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:11
S14	253	345/87.ccls. and temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:24
S15	14378	345/87.ccls. and temperature and switch and control scan adj line	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:25

S16	7	345/87.ccls. and temperature and switch and control and scan adj line	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:34
S17	5	345/204.ccls. and temperature and switch and control and scan adj line	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:35
S18	117	gate adj line adj driver	USPAT; EPO; JPO	OR	OFF	2003/02/26 13:59
S19	24	gate adj line adj driver and current and controller	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:00
S20	1	gate adj line adj driver and current and controller and respond and temperature and change	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:00
S21	3	gate adj line adj driver and current and controller and respond	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:46
S22	1	gate adj line adj driver and current and controller and respond and temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:47
S23	20	gate adj line adj driver and change and temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:48
S24	2	gate adj line adj driver and change adj temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:54
S25	56	respond adj change adj temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 14:56
S26	2	respond adj change adj temperature and current adj controller	USPAT; EPO; JPO	OR	OFF	2003/02/26 15:00
S27	4115	current adj controller	USPAT; EPO; JPO	OR	OFF	2004/01/26 09:40
S28	2	current adj controller and respond adj change adj temperature	USPAT; EPO; JPO	OR	OFF	2003/02/26 15:02
S29	81	current adj controller and (change adj temperature)	USPAT; EPO; JPO	OR	OFF	2003/02/26 15:04
S30	1	"5,754,154".pn.	USPAT; EPO; JPO	OR	OFF	2003/02/27 13:26
S31	1	"5,852,430".pn.	USPAT; EPO; JPO	OR	OFF	2003/02/27 13:27
S32	1	"6,075,511".pn.	USPAT; EPO; JPO	OR	OFF	2003/02/27 13:28
S33	1	"6,313,818" B1.pn.	USPAT; EPO; JPO	OR	OFF	2003/02/27 13:28
S34	2	345/94-101.ccls. and temperature adj detection and current adj detection	USPAT; EPO; JPO	OR	OFF	2003/02/27 13:31
S35	927	345/97.ccls.	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:44

S36	157	345/97.ccls. and current controller and resistor and thermistor and TFT	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:44
S37	156	345/97.ccls. and current controller and resistor and thermistor and TFT and transistor	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:44
S38	2	345/97.ccls. and controller and resistor and transistor	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:46
S39	50	current adj controller and resistor and thermistor and transistor	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:48
S40	50	current adj controller and resistor and thermistor and transistor and temperature and voltage	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:55
S41	20	current adj controller and resistor and thermistor and transistor and temperature and voltage and display	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:15
S42	20	current adj controller and resistor and thermistor and transistor and temperature and voltage and display	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:49
S43	3	current adj controller and resistor and thermistor and matrix and temperature and voltage	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:57
S44	93	resistor and thermistor and matrix and gate and source and drain	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:58
S45	415	resistor and thermistor and LCD	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:58
S46	220	resistor and thermistor and LCD and gate and control	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:24
S47	111	resistor and thermistor and LCD and gate and control and drain	USPAT; EPO; JPO	OR	OFF	2003/03/01 15:59
S48	6	resistor and thermistor and LCD and gate and control and drain and source and pixels	USPAT; EPO; JPO	OR	OFF	2003/03/03 10:29
S49	2	resistor and thermistor and LCD and gate and control and matrix and TFT	USPAT; EPO; JPO	OR	OFF	2003/03/01 16:09
S50	2	resistor and thermistor and LCD and matrix and TFT	USPAT; EPO; JPO	OR	OFF	2003/07/14 17:48
S51	107	resistor and thermistor and LCD and gate and control and drain and source	USPAT; EPO; JPO	OR	OFF	2003/03/02 14:35
S52	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	OR	OFF	2003/03/02 14:50

S53	5	liquid adj crystal adj display and charge adj characteristic and gate	USPAT; EPO; JPO	OR	OFF	2003/03/02 15:07
S54	4	liquid adj crystal adj display and charge adj characteristic and compensate	USPAT; EPO; JPO	OR	OFF	2003/03/02 15:50
S55	10	resistor and thermistor and positive adj temperature adj coefficient and LCD and gate and control	USPAT; EPO; JPO	OR	OFF	2003/03/03 07:52
S56	1860	thermistor and positive adj temperature adj coefficient	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:26
S57	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:26
S58	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:33
S59	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:33
S60	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor and parallel	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:34
S61	10	thermistor and LCD and gate and control and positive adj temperature adj coefficient and resistor and parallel and switch	USPAT; EPO; JPO	OR	OFF	2003/03/03 08:34
S62	12	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 10:09
S63	12	liquid adj crystal adj display and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:24
S64	237	320/116.ccls.	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:24
S65	1	320/116.ccls. and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:27
S66	1	320/116,128.ccls. and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:28
S67	33	batteries and positive adj temperature adj coefficient adj thermistor	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:28

S68	3	batteries and positive adj temperature adj coefficient adj thermistor and LCD	USPAT; EPO; JPO	OR	OFF	2003/03/03 14:28
S69	1	"11273973"	JPO	OR	OFF	2003/03/04 09:13
S70	1	jp-01127973-\$.did.	JPO	OR	OFF	2003/03/04 09:18
S71	1873	gate adj voltage and current and change and controller	USPAT; EPO; JPO	OR	OFF	2003/07/11 10:00
S72	6	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and ambient adj temperature	USPAT; EPO; JPO	OR	OFF	2003/07/11 10:17
S73	11	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit	USPAT; EPO; JPO	OR	OFF	2003/07/11 10:04
S74	3	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and ambient adj temperature and display	USPAT; EPO; JPO	OR	OFF	2003/07/11 11:05
S75	3	ambient adj temperature and gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic and circuit and display	USPAT; EPO; JPO	OR	OFF	2003/07/11 11:06
S76	11	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4 and charge and characteristic	USPAT; EPO; JPO	OR	OFF	2003/07/11 11:07
S77	40	gate adj voltage and current and change and controller and resistor and thermistor and compensat\$4	USPAT; EPO; JPO	OR	OFF	2003/07/11 11:07
S78	72	gate adj voltage and current and change and controller and resistor and thermistor	USPAT; EPO; JPO	OR	OFF	2003/07/11 11:08
S79	1	liquid adj crystal adj display and charge and characteristic adj compensat\$4 adj circuit	USPAT; EPO; JPO	OR	OFF	2003/07/11 14:45
S80	59	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit	USPAT; EPO; JPO	OR	OFF	2003/07/11 14:45

S81	11	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor	USPAT; EPO; JPO	OR	OFF	2003/07/11 14:46
S82	11	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:24
S83	7	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and parallel	USPAT; EPO; JPO	OR	OFF	2003/07/11 14:47
S84	7	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor\$1 and resistor and parallel	USPAT; EPO; JPO	OR	OFF	2003/07/11 16:27
S85	1	charge adj characteristic and compensating adj circuit	USPAT; EPO; JPO	OR	OFF	2003/07/11 16:30
S86	11	charge adj compensating adj circuit	USPAT; EPO; JPO	OR	OFF	2003/07/11 16:30
S87	1	charge adj compensating adj circuit and display	USPAT; EPO; JPO	OR	OFF	2003/07/11 16:31
S88	4	charge adj compensating adj circuit and temperature	USPAT; EPO; JPO	OR	OFF	2003/07/14 08:19
S89	1	charge adj compensating adj circuit and temperature and resistor	USPAT; EPO; JPO	OR	OFF	2003/07/14 08:20
S90	1	"5,250,937".pn.	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:11
S91	1	"5,250,937".pn. and current	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:12
S92	1	"5,250,937".pn. and current and control	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:12
S93	1	"5,250,937".pn. and current and control\$4	USPAT; EPO; JPO	OR	OFF	2003/07/14 13:55
S94	1	current adj controller and resistor and thermistor and charge and characteristic and temperature and voltage and display	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:15
S95	11	current adj controller and resistor and thermistor and charge and temperature and voltage and display	USPAT; EPO; JPO	OR	OFF	2003/07/14 09:22
S96	1	"5,059,767".pn. and current	USPAT; EPO; JPO	OR	OFF	2003/07/14 17:35

S97	1	"5,754,154".pn. and current	USPAT; EPO; JPO	OR	OFF	2003/07/14 17:36
S98	50	current adj controller and resistor and thermistor and transistor and temperature	USPAT; EPO; JPO	OR	OFF	2003/07/14 18:24
S99	2	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and change adj temperature	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:25
S100	1	liquid adj crystal adj display and charge and characteristic and compensat\$4 adj circuit and thermistor and resistor and change adj temperature and width	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:30
S101	33	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and width	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:31
S102	9	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and path and width and current and data and line	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:37
S103	11	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and path and width and current	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:39
S104	15	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:40
S105	12	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver and data and line	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:40
S106	12	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current and gate and driver and data and line\$1	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:42
S107	31	liquid adj crystal adj display and thermistor and resistor and change and temperature and change and current adj control\$4 and gate and driver and data and line\$1	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:43

S108	1	liquid adj crystal adj display and thermistor and resistor and change adj temperature and change and current adj control\$4 and gate and driver and data and line\$1	USPAT; EPO; JPO	OR	OFF	2003/11/20 08:43
S109	1	"5,119,215".pn.	USPAT; EPO; JPO	OR	OFF	2003/11/20 09:28
S110	398	current adj controller and chang\$5 and width and current and path	USPAT; EPO; JPO	OR	OFF	2004/01/26 09:41
S111	398	current adj controller and (chang\$5 and width and current and path)	USPAT; EPO; JPO	OR	OFF	2004/01/26 09:41
S112	19	current adj controller and (chang\$5 and width and current and path) and data adj line\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 11:43
S113	3	current adj controller and (chang\$5 and width and current and path) and data adj line\$1 and (liquid adj crystal adj display or LCD)	USPAT; EPO; JPO	OR	OFF	2004/01/26 09:45
S114	1	current adj controller and (chang\$5 and width adj current adj path)	USPAT; EPO; JPO	OR	OFF	2004/01/26 09:47
S115	2	(LCD or liquid adj crystal adj display) and (chang\$5 and width adj current adj path)	USPAT; EPO; JPO	OR	OFF	2004/01/26 11:14
S116	2	(var\$6 adj width adj current adj path)	USPAT; EPO; JPO	OR	OFF	2004/01/26 11:08
S117	1	(LCD or liquid adj crystal adj display) and (var\$5 and width adj current adj path)	USPAT; EPO; JPO	OR	OFF	2004/01/26 11:14
S118	3	current adj controller and (var\$6 adj width and current and path) and data adj line\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:19
S119	396	gate adj line\$1 adj driv\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:20
S120	181	gate adj line\$1 adj driv\$6 and receiv\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:21
S121	169	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:21
S122	118	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:22
S123	81	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:22

S12 4	26	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:22
S12 5	8	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:23
S12 6	8	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:24
S12 7	4	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:27
S12 8	1	gate adj line\$1 adj driv\$6 and receiv\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and control\$6 adj signal\$1 and gate adj Line\$1 adj control\$6 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:46
S12 9	1	gate adj line\$1 adj driv\$6 and receiv\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6 and gate adj Line\$1 adj control\$6 and (LCD or liquid adj crystal adj panel)	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:52
S13 0	1	gate adj line\$1 adj driv\$6 adj receiv\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:53
S13 1	9	gate adj line\$1 adj (driv\$6 and receiv\$6)	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:53
S13 2	1	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and (gate adj line\$1 adj control\$6)	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:55
S13 3	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:58
S13 4	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:58

S13 5	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 12:59
S13 6	5	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermin\$5 and voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/28 12:41
S13 7	6	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1	USPAT; EPO; JPO	OR	OFF	2004/01/26 16:52
S13 8	645	current adj path and temperature and thermistor	USPAT; EPO; JPO	OR	OFF	2004/01/26 16:53
S13 9	530	current adj path and temperature and thermistor and resistor	USPAT; EPO; JPO	OR	OFF	2004/01/26 16:54
S14 0	8	(var\$6 adj width and current adj path) and temperature and thermistor and resistor	USPAT; EPO; JPO	OR	OFF	2004/01/26 16:59
S14 1	939	345/204.ccls.	USPAT; EPO; JPO	OR	OFF	2004/01/27 08:48
S14 2	44	temperature and width adj current adj path	USPAT; EPO; JPO	OR	OFF	2004/01/27 15:02
S14 3	2	temperature and width adj current adj path adj var\$5	USPAT; EPO; JPO	OR	OFF	2004/01/27 08:51
S14 4	6	temperature and width adj current adj path adj increas\$5	USPAT; EPO; JPO	OR	OFF	2004/01/27 08:54
S14 5	6	width adj current adj path adj increas\$5	USPAT; EPO; JPO	OR	OFF	2004/01/27 08:54
S14 6	3	temperature and width adj current adj path and display	USPAT; EPO; JPO	OR	OFF	2004/01/27 15:02
S14 7	15	gate adj line\$1 adj driv\$6 and receiv\$6 and control\$6 and supply\$6 and predetermine\$5 adj voltage\$1 and accord\$6	USPAT; EPO; JPO	OR	OFF	2004/01/28 09:35
S14 8	5	gate adj line\$1 adj (driv\$6 and receiv\$6) and Control\$6 adj signal\$1 and supply\$6 and predetermin\$5 and voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/28 16:45
S14 9	57	gate adj line\$1 adj driv\$6 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/28 16:46
S15 0	29	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1	USPAT; EPO; JPO	OR	OFF	2004/01/28 16:46
S15 1	9	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1 and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	OR	OFF	2004/06/03 14:28

S15 2	10	gate adj line\$1 adj driv\$6 and receiv\$6 and predetermin\$5 adj voltage\$1 and vari\$9 and Control\$6 adj signal\$1 and supply\$6	USPAT; EPO; JPO	OR	OFF	2004/06/03 14:47
S15 3	15	gate adj line\$1 and control\$9 and thermistor	USPAT; EPO; JPO	OR	OFF	2004/06/03 14:49
S15 4	13	gate adj line\$1 and control\$9 and thermistor and resistor	USPAT; EPO; JPO	OR	OFF	2004/06/03 14:49
S15 5	5	gate adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature	USPAT; EPO; JPO	OR	OFF	2004/06/03 14:50
S15 6	4	gate adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	OR	OFF	2004/06/03 15:38
S15 7	7	scan adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	OR	OFF	2004/06/03 15:39
S15 8	7	scan adj line\$1 and control\$9 and thermistor and resistor and ambient adj temperature and voltage and vari\$6	USPAT; EPO; JPO	OR	OFF	2004/06/03 15:39
S15 9	5	liquid adj crystal adj display and thermistor and resistor and control\$9 and voltage\$1 adj change\$9 and temperature adj ambient	USPAT; EPO; JPO	OR	OFF	2004/06/03 16:05
S16 0	31	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:51
S16 1	3	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and resistor and control\$9 and voltage\$1 and change\$9 and temperature and ambient	USPAT; EPO; JPO	OR	OFF	2004/06/03 16:07
S16 2	10	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and resistor	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:43
S16 3	5	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and resistor and gate	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:44
S16 4	2	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and TFT	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:53

S16 5	0	(liquid adj crystal adj display or LCD) and scan adj line\$1 and thermistor and gate adj line\$1	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:53
S16 6	1	(liquid adj crystal adj display or LCD) and scan and thermistor and gate adj line\$1	USPAT; EPO; JPO	OR	OFF	2004/06/04 13:54
S16 7	1	(liquid adj crystal adj display or LCD) and scan and thermistor and gate adj line\$1 and voltage\$1 and control\$9	USPAT; EPO; JPO	OR	OFF	2004/06/04 14:00
S16 8	7	(liquid adj crystal adj display or LCD) and thermistor and gate adj line\$1 and voltage\$1 and control\$9 and vari\$9 and resistor	USPAT; EPO; JPO	OR	OFF	2004/06/04 14:30
S16 9	0	(liquid adj crystal adj display or LCD) and scan adj line\$1 adj voltage\$1 and control\$9 and vari\$9 and thermistor	USPAT; EPO; JPO	OR	OFF	2004/06/04 14:31
S17 0	0	(liquid adj crystal adj display or LCD) and scan adj line\$1 adj voltage\$1 and control\$9 and thermistor	USPAT; EPO; JPO	OR	OFF	2004/06/04 14:31
S17 1	14	(liquid adj crystal adj display or LCD) and scan adj line\$1 adj voltage\$1 and control\$9 and vari\$9	USPAT; EPO; JPO	OR	OFF	2004/06/04 14:32
S17 2	41407	display and (TFT or thin adj film adj transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:26
S17 3	5801	display and (TFT or thin adj film adj transistor) and gate adj line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:26
S17 4	182	display and (TFT or thin adj film adj transistor) and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:27
S17 5	17	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:27

S17 6	6	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:46
S17 7	0	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor and thermister and connect\$6 and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:31
S17 8	1	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor\$1 and thermistor\$1 and connect\$6 and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 14:32
S17 9	5	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor\$1 and thermistor\$1 and connect\$6 and gate and line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:00
S18 0	3	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor\$1 and thermistor\$1 and connect\$6 and gate adj line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:01
S18 1	3	display and (TFT or thin adj film adj transistor) and DC and voltage\$1 and convert\$6 and connect\$6 and resistor\$1 and thermistor\$1 and connect\$6 and gate adj line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:06
S18 2	3	display and (TFT or thin adj film adj transistor) and DC and convert\$6 and connect\$6 and resistor\$1 and thermistor\$1 and connect\$6 and gate adj line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:07
S18 3	1	display and (TFT or thin adj film adj transistor) and DC and voltage and converter and connect\$6 and resistor and temperature and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:47
S18 4	12	display and (TFT or thin adj film adj transistor) and connect\$6 and resistor and temperature and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:49

S18 5	1	display and (TFT or thin adj film adj transistor) and connect\$6 and resistor and thermistor and gate adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:49
S18 6	0	display and (TFT or thin adj film adj transistor) and connect\$6 and resistor and thermistor and scan adj line adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:51
S18 7	0	display and (TFT or thin adj film adj transistor) and connect\$6 and resistor and thermistor and scan adj line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:50
S18 8	0	display and (TFT or thin adj film adj transistor) and resistor\$1 and thermistor\$1 and scan adj line and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:50
S18 9	9	display and (TFT or thin adj film adj transistor) and resistor\$1 and thermistor\$1 and scan\$6 adj line\$1 and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 15:51
S19 0	1	display and (TFT or thin adj film adj transistor) and connect\$6 and resistor\$1 and thermistor\$1 and scan\$6 adj line\$1 adj driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:14
S19 1	0	display and (TFT or thin adj film adj transistor) and (connect\$6 and resistor\$1 and thermistor\$1 and scan\$6 adj line\$1) adj driv\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:15
S19 2	4	display and (TFT or thin adj film adj transistor) and (connect\$6 and resistor\$1 and thermistor\$1 and scan\$6 adj line\$1 adj driv\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:37
S19 3	2	"20040027508"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:28
S19 4	1	"20040027508" and thermistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:28

S19 5	8	display and (connect\$6 and resistor\$1 and thermistor\$1 and scan\$6 adj line\$1 adj driv\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/18 16:38
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